

IN THE UNITED STATES PATENT OFFICE

Applicants: Tim Tuan et al.

Assignee: Xilinx, Inc.

Title: Tuning Programmable Logic Devices for Low-Power Design
Implementation

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the two references listed in the attached Substitute for Form 1449A/PTO.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above document shall not be construed as an admission that the document is necessarily prior art with respect to the instant invention. Citation of the above document shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above document shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

There is no fee due with this filing, but the Commissioner is hereby authorized to charge any fees or credit any overpayment which may be required to Deposit Account No. 24-0040.

Respectfully submitted,

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*I hereby certify that this correspondence is being filed via EFS-Web with
the United States Patent & Trademark Office on October 3, 2008.*

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